

PN	Core	Cordic	HCL	Memory		Signal Chain		Control device							PBUS	Interface							GPIO	Package	Operating Temperature Range	Package qty
				SRAM	FLASH	ADC	DAC	HRPWM/EPWM/PWM	CAP	QEP	QOUT	CPM	SDFM	TA-IF/BISS-IF/ENDAT-IF		USB	EMAC	UART	QSPI	SPI	CAN	I ² C				
M6806SPEA	E907 500MHz FPU+DSP	Y	Y	1MB	2MB	12bit 4Msps x2/16CH	N	6CH/12CH/24CH	x6	x10	x2	x2	x4	x2/x2/x2	Y	1	1	8	2	3	2	3	84	LQFP100 14x14mm,0.5mm	-40~85°C	盘90 包900
M6806SPES	E907 500MHz FPU+DSP	Y	Y	1MB	2MB	12bit 4Msps x2/16CH	N	6CH/12CH/24CH	x6	x10	x2	x2	x4	x2/x2/x2	Y	1	1	8	2	3	2	3	84	LQFP100 14x14mm,0.5mm	-40~125°C	盘90 包900
M6806SPEQ	E907 500MHz FPU+DSP	Y	Y	1MB	2MB	12bit 4Msps x2/16CH	N	6CH/12CH/24CH	x6	x10	x2	x2	x4	x2/x2/x2	Y	1	1	8	2	3	2	3	84	LQFP100 14x14mm,0.5mm	-40~125°C	盘90 包900
M6806SNEA	E907 500MHz FPU+DSP	Y	Y	1MB	2MB	12bit 4Msps x2/16CH	N	6CH/12CH/24CH	x6	x10	x2	x2	x4	x2/x2/x2	Y	1	1	8	2	3	2	3	88	QFN100 12x12mm,0.4mm	-40~85°C	盘189 包1890
M6806SNES	E907 500MHz FPU+DSP	Y	Y	1MB	2MB	12bit 4Msps x2/16CH	N	6CH/12CH/24CH	x6	x10	x2	x2	x4	x2/x2/x2	Y	1	1	8	2	3	2	3	88	QFN100 12x12mm,0.4mm	-40~125°C	盘189 包1890
M6805SPDA	E907 500MHz FPU+DSP	Y	Y	512KB	1MB	12bit 4Msps x2/15CH	N	6CH/9CH/18CH	x4	x6	x1	x2	x2	x1/x1/x1	Y	1	0	6	1	2	1	2	65	LQFP80 10x10mm,0.4mm	-40~85°C	盘160 包1600
M6805SPDS	E907 500MHz FPU+DSP	Y	Y	512KB	1MB	12bit 4Msps x2/15CH	N	6CH/9CH/18CH	x4	x6	x1	x2	x2	x1/x1/x1	Y	1	0	6	1	2	1	2	65	LQFP80 10x10mm,0.4mm	-40~125°C	盘160 包1600
M6805SNDA	E907 500MHz FPU+DSP	Y	Y	512KB	1MB	12bit 4Msps x2/15CH	N	6CH/9CH/18CH	x4	x6	x1	x2	x2	x1/x1/x1	Y	1	0	6	1	2	1	2	67	QFN80 9x9mm,0.4mm	-40~85°C	盘260 包2600
M6805SNDS	E907 500MHz FPU+DSP	Y	Y	512KB	1MB	12bit 4Msps x2/15CH	N	6CH/9CH/18CH	x4	x6	x1	x2	x2	x1/x1/x1	Y	1	0	6	1	2	1	2	67	QFN80 9x9mm,0.4mm	-40~125°C	盘260 包2600
M6802SNCA	E907 500MHz FPU+DSP	Y	N	512KB	1MB	12bit 4Msps x2/15CH	N	6CH/6CH/12CH	x3	x5	x1	x0	x2	x1/x1/x1	N	0	0	5	1	2	1	2	54	QFN64 7x7mm,0.35mm	-40~85°C	盘260 包2600
M6802SNCS	E907 500MHz FPU+DSP	Y	N	512KB	1MB	12bit 4Msps x2/15CH	N	6CH/6CH/12CH	x3	x5	x1	x0	x2	x1/x1/x1	N	0	0	5	1	2	1	2	54	QFN64 7x7mm,0.35mm	-40~125°C	盘260 包2600
M6801SPCA	E907 500MHz FPU+DSP	Y	Y	512KB	1MB	12bit 4Msps x2/15CH	N	6CH/6CH/12CH	x3	x5	x1	x2	x2	x1/x1/x1	N	0	0	5	1	1	1	2	49	LQFP64 10x10mm,0.5mm	-40~85°C	盘160 包1600
M6801SPCS	E907 500MHz FPU+DSP	Y	Y	512KB	1MB	12bit 4Msps x2/15CH	N	6CH/6CH/12CH	x3	x5	x1	x2	x2	x1/x1/x1	N	0	0	5	1	1	1	2	49	LQFP64 10x10mm,0.5mm	-40~125°C	盘160 包1600